



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Richmond et al.

Assignee: ZiLOG, Inc.

Title: "Clock Controller with Clock Source Fail-Safe Logic"

Appl. No.: 10/764,391

Filing Date: January 23, 2004

Examiner: James F. Sugent

Art Unit: 2116

Docket No.: ZIL-555

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July 21, 2007

Mail Stop Amendment  
COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE TO OFFICE ACTION**

Dear Sir:

In response to the non-final office action dated June 20, 2007 ("Office Action"), Applicants respond to the rejections in the non-final office action as follows.

There are no amendments to the specification in this Response.

There are no amendments to the claims in this Response.

There are no amendments to the drawings in this Response.

The **Remarks** begin on page 7 of this Response.